

Signed Integer Numbers

3

Fixed Length Numbers

Show Memory

• byte, consecutive bytes.

- Computer stores numbers in fixed-length memory locations.

Example : integer are usually stored in ~~the~~ memory location consisting of 4 consecutive bytes (total 32 bits).

- The amount of memory used to represent one type of data is fixed.

Example :

type of data	amount of memory used
character	1 byte
integer	4 bytes.
float	4 bytes
double	8 bytes.
short	2 bytes.

- Unsigned number = sequence of bits of number viewed as corresponding to powers of 2.

eg $1010 = 1 \times 2^3 + 0 \times 2^2 + 1 \times 2^1 + 0 \times 2^0$
 $= 8 + 2 = 10.$

Representing & Using signed numbers

- Methods used to represent signed numbers in computers:
(fixed length !!!).

(1) sign / magnitude representation.

(2) two's complement

(3) one's complement (obsolete).

(4) excess 2^{m-1} ($m = \# \text{ bits used}$).

skip

Sign / magnitude representation:

- people is accustomed to this representation,

e.g.:

$$\begin{array}{rcl} +4 & = & \text{pos. } 4 \\ -4 & = & \text{neg. } 4. \end{array}$$

} very clumsy !!
=====

The number is represented by a sign & its absolute value (magnitude).

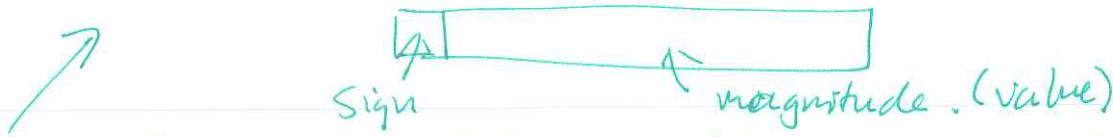
- An analogic representation is available in binary.

The sign (+/-) is represented by a bit 0/1

$$0 = +$$

$$1 = -$$

so the INTERPRETATION is:



In general, the leftmost bit in a binary number is chosen as the sign bit.

e.g. With 8 bits, we can interpret the numbers:

$$\begin{array}{c} \text{as: } \boxed{0} 0001101 \\ \uparrow \qquad \qquad \qquad \text{magnitude} = 0 + 4 + 1 \\ \text{Sign} = + \\ \qquad \qquad \qquad = 13. \end{array}$$

And:

$$\text{number} = +13.$$

$$\begin{array}{c} \text{as: } \boxed{1} 000.1101 \\ \uparrow \qquad \qquad \qquad \text{magnitude} = 13. \\ \text{Sign} = - \end{array}$$

$$\text{number} = -13$$

- ~~How~~ What numbers can be represented with 8 bits?

$$\begin{array}{c} \boxed{0} 1 1 1 1 1 1 \\ \uparrow \qquad \qquad \qquad \text{magn.} \leq 2^7 + 2^6 + 2^5 + 2^4 + 2^3 + 2^2 + 2^1 + 2^0 \\ \text{Sign} \\ \qquad \qquad \qquad = 127. \end{array}$$

numbers represented $[-127, 127]$.

Note: What value a bit pattern represents depends on the context

e.g.: if we are given a bit pattern:

1 0 0 0 1 1 0 1

But not given the context under which the pattern is considered, we cannot know the value represented.

How do we specify the context: depends on the programming environment.

In C:

① unsigned char X;

if X contains:

1 0 0 0 1 1 0 1

the value represented (stored in) by X is

$$\begin{aligned} 2^7 + 0 + 4 + 1 &= 128 + 0 + 4 + 1 \\ &= 141 \end{aligned}$$

② char X

Now X is signed! different context!

Problems with sign/magnitude representation:

(1) 2 different patterns represent the value of

$$\begin{array}{r} + \emptyset \\ - \emptyset \end{array}$$

0	000	...	0
1	000	-	0

testing for ZERO is complicated....

(2) Addition & subtraction are not uniform,
but depends on values of operands.

e.g:

$$\begin{array}{r} 5 \\ + 3 \\ \hline 8 \end{array}$$

is an addendum

$$\begin{array}{r} 0000.0101 \\ 0000.0011 \\ \hline 0000.1000 \end{array} +$$

but:

$$\begin{array}{r} 5 \\ + -3 \\ \hline 2 \end{array}$$

is actually a subtraction.

Convert

$$\begin{array}{r} 0000.0101 \\ + 0000.0011 \\ \hline 0000.0010 \end{array}$$

Stretch representation (& context)

Odometer numbers: intro to complementary arithmetic

- Five-digit mileage indicator (odometer) on exercise cycle.

0 1 2 4 9

- Initial odometer reading = 00000

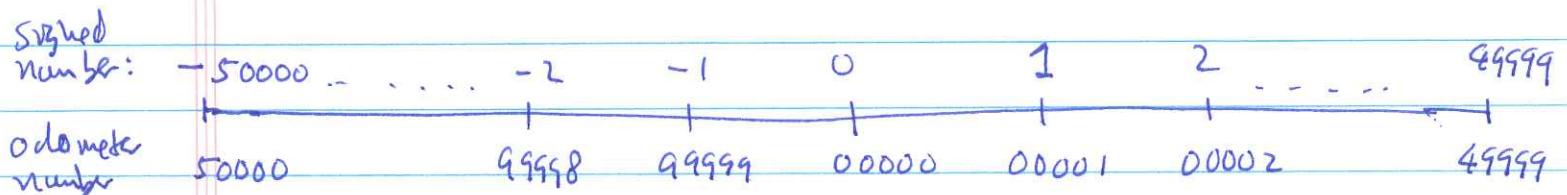
- Pedal forward : +1

Pedal backward : -1.

<u>Odometer Reading</u>	<u>Distance Traveled</u>
:	
00002	2 miles forward
00001	1 mile forward
00000	Initial reading
99999	1 mile backward
99998	2 miles backward
:	

- The concept of distance in one of 2 opposite directions is the basis of signed numbers in arithmetic.

- Representation of signed numbers:



- Problem :

An odometer number 60000 could be a result of:

(1) 60000 miles forwards. $\rightarrow +60000$

(2) 40000 miles backwards... $\rightarrow -40000$

- Some convention is needed to give each odometer number a unique value:

We split the range of possible numbers

$00000 \dots 99999$

into 2 "equal" parts:

0-4 first digit $\rightarrow 00000 \dots 49999$

represent "positive" numbers

5-9 first digit $\rightarrow 50000 \dots 99999$

represent "negative" numbers

forward

backwards.

Adding odometer numbers :

$$\begin{array}{r}
 99998 \\
 + 00003 \\
 \hline
 100001
 \end{array}
 \quad = \quad " - 2 "$$

= + " + 3 "
 + 1

↗
discarded.

Subtraction :

$$\begin{array}{r}
 99995 \\
 - 00003 \\
 \hline
 99992
 \end{array}
 \quad = \quad " - 5 "$$

- " + 3 "
 = " - 8 "

Inverting a number:

$$00003 = +3.$$

$$99997 = -3.$$

To invert 00003 : subtract it from 100000.

To invert 99997 : also subtract it from 100000.

Usage: What value is associated with odometer number 04500 ?

04500 is a positive odometer number
read it as the value itself.

What value is associated with odometer number 55000 ?

55000 is a negative odometer number.
Invert:

$$\begin{array}{r} 100000 \\ - 55000 \\ \hline 45000. \end{array}$$

Converting:

odometer

negative value is equal to 45000.

+ value	→ decimal
- value	→ [00000-decimal]
Actual value	← [0..4] repr.
- (100000-repr)	← [5..9] repr.

The value is then equal to -45000.

- Odometer numbers are in fact called:

10's complement numbers.

* Overflow:

Due to the fact that the number of possible values represented is limited, overflow will/can occur.

eg:

$$\begin{array}{r} 04992 \\ + 00001 \\ \hline 50001 \end{array} \quad = \quad 4992.$$

↓

it represents a negative value!

dom/int.c

$$\begin{array}{r} 100000 \\ - 5000 \\ \hline 49999 \end{array}$$

Value represented = -49999.

The addition took 2 positive values & produced a negative value.

We said there was an overflow (more than the representation can handle → it can handle positive numbers upto 49999, result is *50001, more than the representation can handle).



Save effect as: old car: 49999

drive one mile → new car! 00000 !!!

10's complement encoding of signed DECIMAL numbers:

- Complement of digit d = $10 - d$.

e.g.: 223 digits. $9 \leftrightarrow 10$... $5 \leftrightarrow 4$.
 $8 \leftrightarrow 1$...

- To negate a number, take its digit wise complement and add 1.

- e.g.: 3 digits

$$+ 123 = 123.$$

$$- 123$$

$$+ 123$$

\downarrow Complement.

$$\begin{array}{r} 876 \\ + 1 \\ \hline 877 \end{array}$$

Same as

$$\begin{array}{r} 1000 \\ - 123 \\ \hline 877 \end{array}$$

So "877" represents -123 !!!

Subtract from 1000

- The left most digit is a sign digit:

digit $\geq 5 \rightarrow$ number is negative.

digit $\leq 4 \rightarrow$ number is positive.

Only one representation for \emptyset : 000.

$$+0 = 000$$

$$-0 = 000$$

$$\begin{array}{r} & & \downarrow & \text{complement} \\ & 9 & 9 & 9 \\ & + & 1 \\ \hline 1 & 0 & 0 & 0 \\ \swarrow & & & \\ \text{discard.} & & & \end{array}$$

- Eg. What does 821 represent?

(1) find sign : neg.

(2) find value : 821 ↓ compl.

$$\begin{array}{r} 178 \\ -1 \\ \hline 179 \end{array} \Rightarrow \underline{\underline{-179}}$$

Now:

Humans

Encoding.

$$\begin{array}{r} 321 \\ + -123 \\ \hline 198 \end{array} \rightarrow \begin{array}{r} 321 \\ + 877 \\ \hline \textcircled{1} 198 \end{array} = 198.$$

~~8~~
discard

The magic works through ENCODING
(modulo-calculus).

↓ modulo calculus.

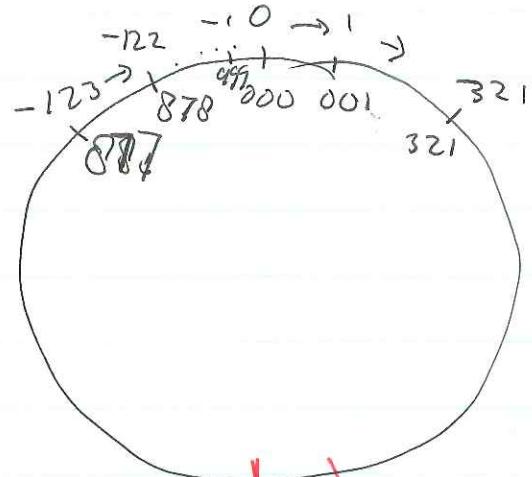
$$\begin{array}{r} 321 \\ + -123 \\ \hline \textcircled{1} \end{array} \quad \left| \begin{array}{r} 321 \\ 877 \\ \hline \end{array} \right.$$

(2) $\begin{array}{r} 321 \\ + (1000-123) \\ \hline \end{array}$

actually:

$$\begin{array}{r} 321 \\ -123 \\ \hline 198 \end{array} \Rightarrow \begin{array}{r} 321 \\ + -123 \\ \hline \end{array}$$

Explanation:



500 499
→ overflow can happen here also.

Example:

$$\begin{array}{r} \textcircled{1} 2 3 \\ - 3 2 1 \\ \hline 8 0 2 \end{array}$$



"802" represents
 -198 .

$\delta \geq 5 \rightarrow$ number neg.

Invert:

802



complement

$$\begin{array}{r} 197 \\ + 1 \\ \hline 198 \end{array}$$

Thus: ~~it is~~

"802" represents -198 .

How it is computed: by humans.

$$\begin{array}{r} 321 \\ - 123 \\ \hline 198 \end{array}$$

The result is negative
thus: -198 .

Two's Complement Binary Numbers.

- Concepts for 10's complementary numbers (odometer numbers) can be extended to fixed-length binary numbers.

The resulting representation is called "Two's Complementary representation")

- Assume we have 8 bit fixed length binary numbers.

<u>Two's Complementary number</u>	<u>Value represented by the number</u>
00000000	3_{10}
00000010	2_{10}
00000001	1_{10}
00000000	0_{10}
11111111	-1_{10}
11111110	-2_{10}
11111101	-3_{10}
11111100	-4_{10}
:	:

- The range of possible values:

$$0000.0000 - 1111.1111$$

is divided into 2 "equal" pieces:

all numbers starts with 0 0000.0000 - 0111.1111 represents the values from 0 - 127.

all starts with 1 → 1000.0000 - 1111.1111 represents negative values (-1 to -128).

1111.1111 represents -1_{10} .

1000.0000 represents -128_{10} .

- How to negate a 2's complement number?

e.g.: 0000.0001 represents +1.

What is the 2's complement repr. for the number -1?

$$\begin{array}{r} \text{Dv: } 10000.0000 \\ - 0000.0001 \\ \hline 1111.1111 \end{array}$$

→ represents -1.

- How to obtain the value represented by a 2's complement number?

- (1) if number is positive (first digit = 0), use the formula :

$$\sum_{i=0}^N a_i \cdot 2^i$$

- (2) if number is negative (first digit = 1), do:

- (2a) ~~first the~~ negate the number. - ~~result is pos~~ result is pos
- (2b) apply (1). above.

Example: $0001.1011_2 = ?$

$$\begin{aligned}\text{Answer} &= 2^4 + 2^3 + 2^1 + 2^0 \\ &= 16 + 8 + 2 + 1 \\ &= 27.\end{aligned}$$

Example: $1110.0101_2 = ?$

Answer : get inverse : 10000.0000

$$\begin{array}{r} - 1110.0101 \\ 0001\ 1111 \end{array}$$

$\cancel{1}$

$$16 + 8 + 2 + 1 = 27.$$

final answer = -27 .

Binary Arithmetic in two's complement:

- When ^{signed} numbers are represented in two's complement, addition of signed numbers do not require special conversion. **Think: people operate on signed numbers in different ways.**

- Example: (8 bits) With 8 bits

$$\begin{array}{r} \text{ADD} \\ \hline 3 & 0000.0010 \\ + 5 & + 0000.0101 \\ \hline 8 & 0000.1000 \end{array}$$

Actually.

$$\begin{array}{r} \text{SUBTRACT} \\ \hline -5 \\ -3 \\ \hline -2 \end{array}$$

$$\begin{array}{r} 0000.0011 \\ + 111.1011 \\ \hline 1111.1100 \end{array}$$

$$\begin{array}{r} 0000.0100 \\ 1111.0010 \\ + 1 \\ \hline 1111.1011 \end{array}$$

$$\begin{array}{r} \downarrow \text{reg.} \\ 0000.0001 \\ + 1 \\ \hline 0000.0010 = 2 \end{array} \Rightarrow \boxed{-2}$$

$$\begin{array}{r} -3 \\ + 5 \\ \hline 2. \end{array} \quad \begin{array}{r} 1111.1101 \\ + 0000.0100 \\ \hline 0000.0010 = 2. \end{array}$$

↑
actually
Subtract
↓ discard

$$\begin{array}{r} 0000.0011 \\ 1111.1100 \\ + 1 \\ \hline 1111.1101 \end{array}$$

actually ADD!!

↓

$$\begin{array}{r} -3 \\ + -5 \\ \hline -8. \end{array}$$

$\begin{array}{r} 1111.1101 \\ + 1111.1011 \\ \hline 1111.1000 \end{array}$

(1) ↓

↑
discard.

↓ neg.

$$\begin{array}{r} 0000.0111 \\ + 1 \\ \hline 0000.1000 = 0. \end{array}$$

→ -8 .

$$\begin{array}{r} 3 \\ -5 \\ \hline - \end{array}$$

$\begin{array}{r} 0000.0011 \\ - 0000.0101 \\ \hline \cancel{1111.1010} \end{array}$

$1111.1\cancel{0}10 = -2$

$$\begin{array}{r} 3 \\ - (-5) \\ \hline - \end{array}$$

$\begin{array}{r} 0000.0011 \\ - 1111.1011 \\ \hline 00001000 \end{array}$

$= 0.$

$$\begin{array}{r} -3 \\ - 5 \\ \hline - \end{array}$$

$\begin{array}{r} 1111.1101 \\ - 0000.0101 \\ \hline 11111000 \end{array}$

$= -8.$

$$\begin{array}{r} -3 \\ - (-5) \\ \hline - \end{array}$$

$\begin{array}{r} 1111.1101 \\ - 1111.1011 \\ \hline 00000010 \end{array}$

$= 2.$

demo / not 1.c

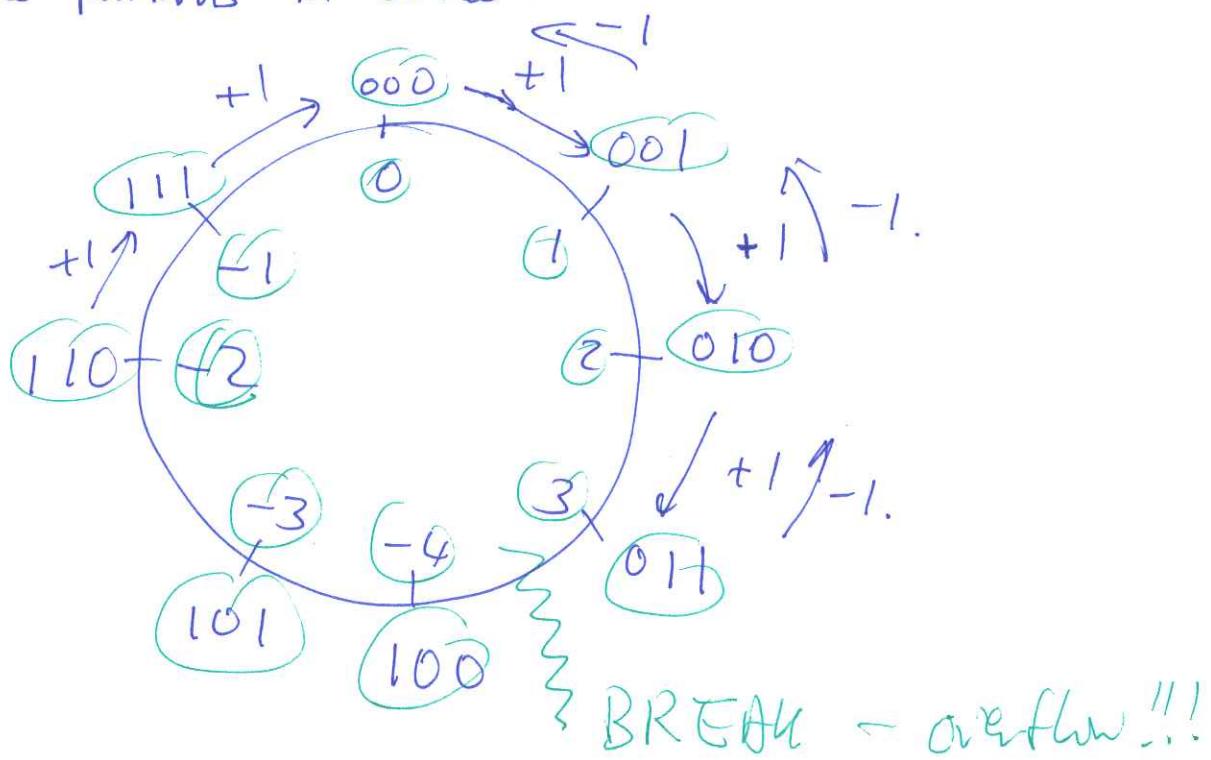
How is this "magic" achieved?

Consider the case with $n=3$ bits.

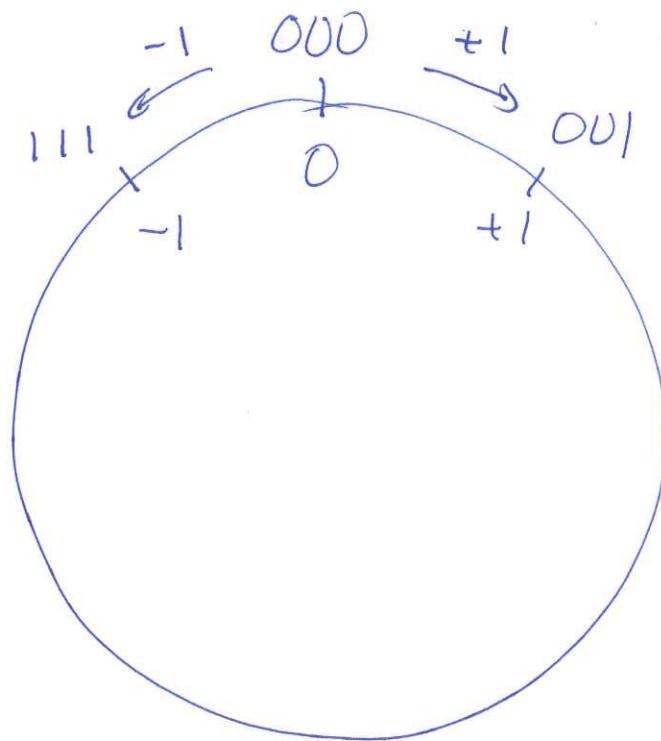
The 3-bit patterns represent the following numbers:

<u>bit pattern</u>	<u>signed decimal value</u>
000	= 0
001	= 1
010	= 2
011	= 3
100	= -4
101	= -3
110	= -2
111	= -1.

List the patterns in circle:



Draw like this:



and so on.

- 111 represents -1 , so that when 1 is added to 111, we get $1000 \rightarrow 000$

which represents the addition: $\begin{array}{r} -1 \\ + 1 \\ \hline 0 \end{array}$.

- Add 1 = move from pattern to the pattern on its right.

Sub 1 = move from pattern to the pattern on its left.

~~REVIEW~~

2-complement encoding:

- Advantage : Operation (add, subtract, multiply) on positive AND negative numbers are identical.

(ie: $(\text{pos}) + (\text{neg})$)

is just add

No need to :

(1) convert neg to pos.

(2) subtract.

- This advantage is gained by

ENCODING

NOT by working in the binary system.

- Same advantage is achieved in

"10's complement encoding".

Overflow

- With a fixed (limited) number of digits, it is impossible to represent all possible values.
- Some values cannot be represented by a given number of digits.
- In some arithm. operations, it can result in a value that is not representable.

In these cases, we say "an overflow has occurred" and the result of the operation is INCORRECT!

- Example of overflow:

adding: Overflow can only happen when we add 2 numbers of same sign.

subtract: Overflow can only happen when we subtract 2 numbers of different signs !

Example:

$$\begin{array}{rcl} (100)_{10} & = & 0110.0100 \\ (26)_{10} & = & 0001.1010 \quad + \\ \hline (126)_{10} & = & 0111.1110 \end{array}$$

↑ positive result. no overflow.

$$\begin{array}{rcl} (100)_{10} & = & 0110.0100 \\ (100)_{10} & = & 0110.0100 \quad + \\ \hline & & 1100.1000 \end{array}$$

↑ negative result. overflow detected
pDS + pVS
→ neg result!

1100.1000 represents:

$$\begin{array}{rcl} 1100.1000 & & \\ & \downarrow & \\ 0011.0111 & + 1 & \\ \hline 0011.1000 & = & 8 + 16 + 32 \\ & & = 56 \end{array}$$

-56. mS + 200.

What can we do about overflow?

- Use more bits for representation.

e.g.: the addition $(100)_{10} + (100)_{10}$ will not result in overflow with 16 bits:

$$\begin{array}{r} 0000.0000.0110.0100 \\ + 0000.0000.0110.0100 \\ \hline 0000.0000.1100.1000 \\ \uparrow \qquad \qquad \qquad \text{32} \cdot 16 \quad 8 \cdot 4 = 2 \\ \text{positive result} \end{array}$$

$$8 + 64 + 128 = 200.$$

- Conclusion:

Computer has a need to convert 2's complement representations of various length

e.g.: 8 bits \rightarrow 16 bits \rightarrow 32 bits etc.



Example:

Value :

8 bit 2's compl

16 bit 2's compl repr

3

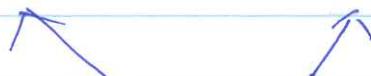
0000.0011

0000.0000.0000.0011

-3

1111.1110

1111.1111.1111.1110



What's the difference
in representation?

How does conversion behave:

Key: the value represented in each representation system
must be the same (unchanged).

- How do we convert: eg: 8 bit \rightarrow 16 bit.

(1) Look at the sign bit in the 8 bit repr.

(2) repeat this signbit into the left most part
of the 16 bit repr.

eg: 0000.0011

\rightarrow 0....0. 0000.0011
repeat 6

1111.1110

\rightarrow 1....1. 1111.1110
repeat 1
16 bit repr.

- Bottom line: extend sign bit towards left.
 - This operation is thus also called "sign extension".

demo/int2.c

Excess encoding

- Let $m = \# \text{ bits}$ in the representation.
- The excess encoding corresponding to m bits is called "excess 2^{m-1} ".

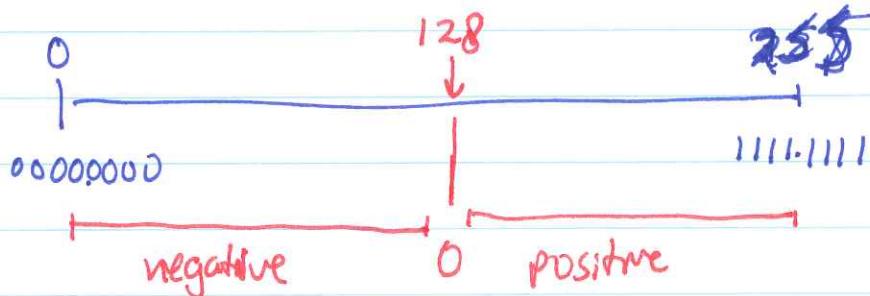
e.g.: $m = 8$ bits.

The excess encoding scheme is: "excess 128".

- In excess 2^{m-1} encoding scheme, the value x is represented by the unsigned value $x + 2^{m-1}$.

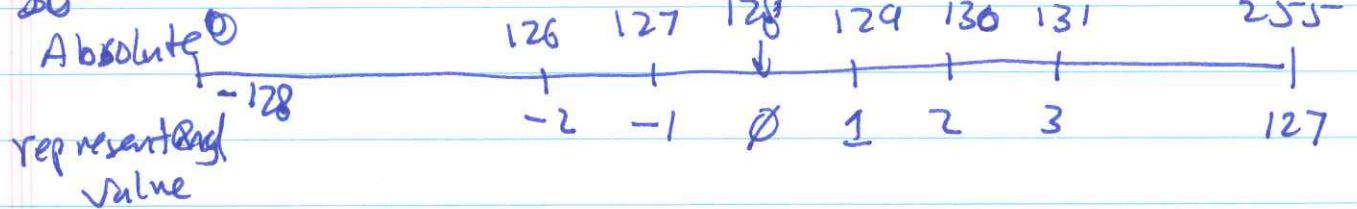
For example: in excess 128 ($m=8$), the value of x is represented by the unsigned value $x + 128$.

Reason: with 8 bits, the unsigned values represented are:

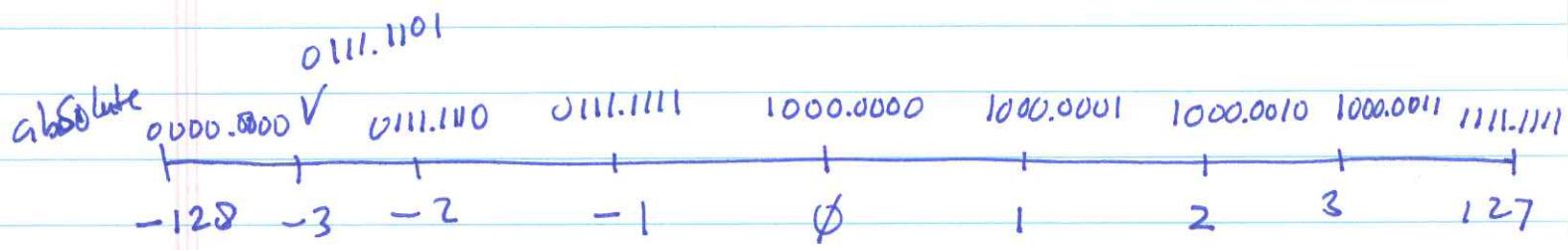


We use half of the numbers to represent negative values and half to represent positive.

So:



- Excess encoding in binary:



Converting a value to its excess 128 representation:

Given value x .

$$\text{Add } 128 \rightarrow x + 128$$

Write $x + 128$ in unsigned representation.

Eg: ~~2*~~

Converting an excess

Eg: $x = 3$:

$$\text{add } 128 \rightarrow 3 + 128 = 131.$$

Write 128 in unsigned : $\frac{128}{2} \vdots$

$$\text{or: } 128 = 1000.0000$$

$$\begin{array}{r} 3 \\ \hline 131 \end{array} \quad \begin{array}{r} 0000.0011 \\ + \\ 1000.0011 \end{array}$$

Eg: $x = -3$:

$$\text{add } 128 \rightarrow -3 + 128 = 125.$$

Write 128 in Unsigned : $\frac{128}{2} \vdots$

$$\text{or: } \begin{array}{r} 128 = 1000.0000 \\ -3 = 0000.0011 \\ \hline 01111101 \end{array}$$

Converting an excess

Finding the value represented by an excess 128 encoding:

(1) Find the ~~value~~ (absolute value). A

(2) Solve: $x + 128 = A$.

to obtain value x.

e.g.: Repr = 0110,1010

$$\begin{aligned}A &= 2^6 + 2^5 + 2^3 + 2^1 \\&= 64 + 32 + 8 + 2 \\&= 106.\end{aligned}$$

Solve: $x + 128 = 106$

$$\begin{aligned}x &= 106 - 128 \\&= -22.\end{aligned}$$

q.

Repr = 1010.0110

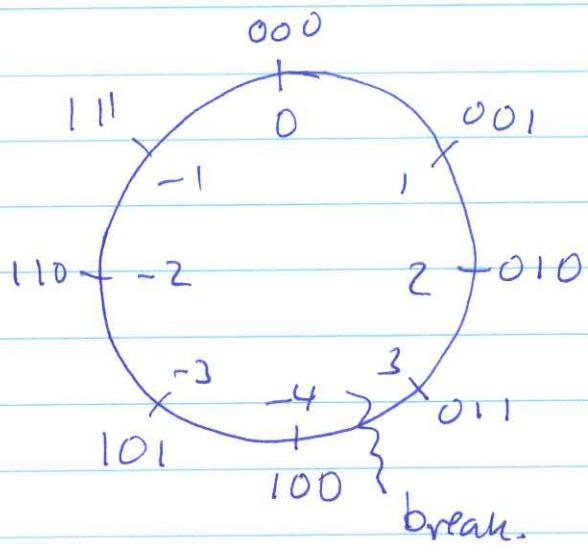
$$\begin{aligned}A &= 2^7 + 2^5 + 2^2 + 2^1 \\&= 128 + 32 + 4 + 2 \\&= 166\end{aligned}$$

$$\text{Solve: } x + 128 = 166$$

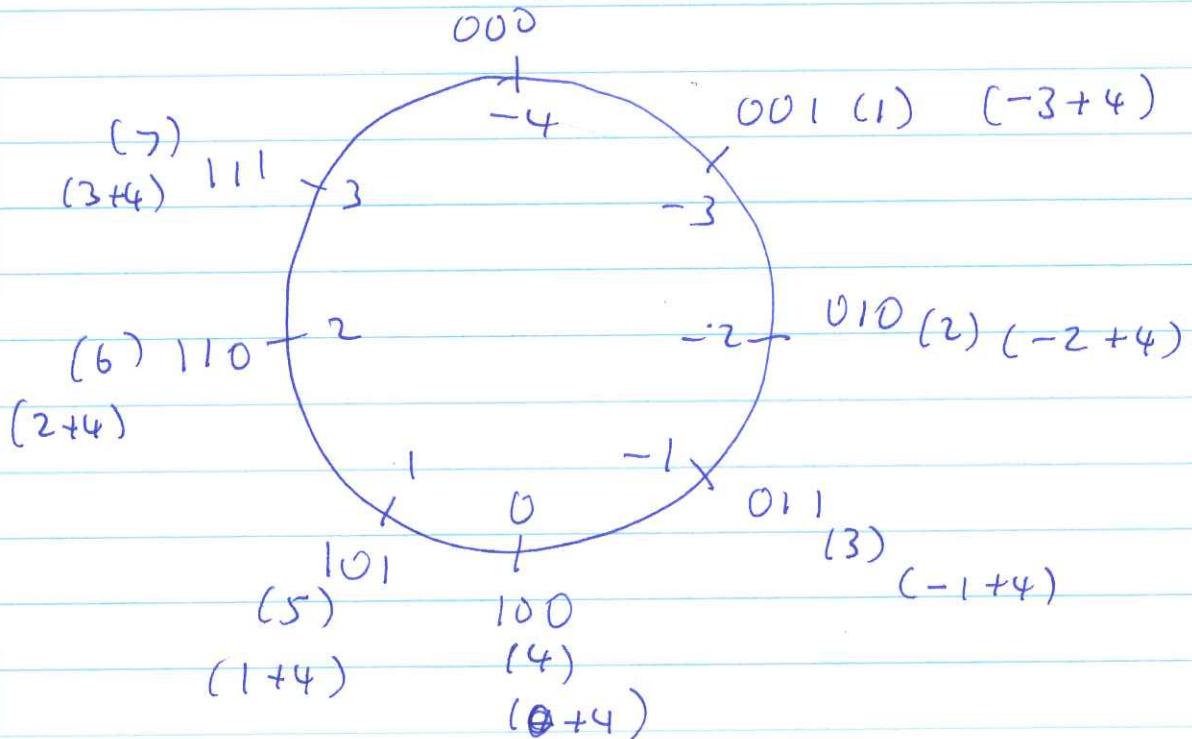
$$\begin{aligned}x &= 166 - 128 \\&= +38.\end{aligned}$$

Excess 2^{m-1} codes are very similar to 2's complement codes in nature:

2's complement



Excess 4: $(2^{3-1} = 2^2)$, $(0) (-4+4)$.



It's shifted.