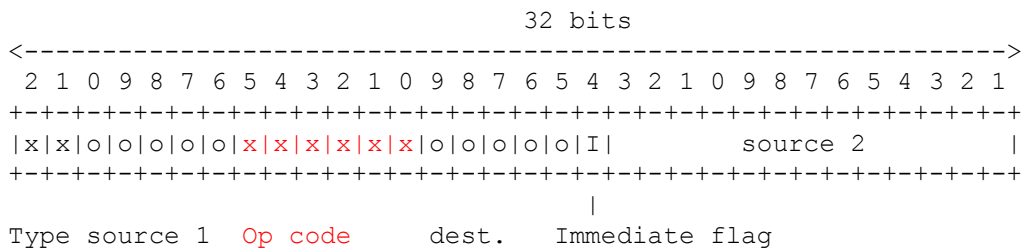
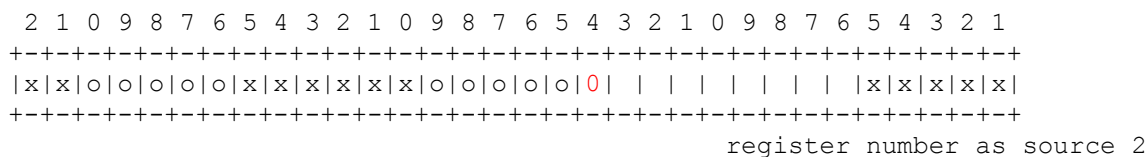


SPARC Instruction Encoding

- SPARC is a "RISC" (Reduced Instruction Set Computer) micro processor (CPU) designed by Sun Microsystems around 1990.
- SPARC has fixed length instruction format:



Instruction with 2 register operands: Immediate flag = 0



Instruction with 1 reg and 1 constant (source 2) operand: Immediate flag = 1

